



PC302 Features

- Implements a complete Femtocell Access Point (FAP) on a single chip
- Includes a complete 3GPP Release 6 WCDMA PHY supporting 14Mbps HSDPA and 5.6Mbps HSUPA together with support for MBMs
- Femto Forum compliant security supporting TPM features for IPsec tunneling, key exchange and authentication.
- Security features for encrypted code imaging to prevent cloning
- Femto Forum compliant FAP lu interfaces with integrated RNC functionality available (Release 7 compliant)
- Hardware support for packet based timing solutions such as IEEE1588 and NTPv3.0.
- Timing synchronization software solutions available
- 3G and 2G network listen support to enable zero-touch provisioning and interference mitigation (SON TR32.821)
- RNC Stack Management for handoff management, congestion control, access control etc
- Femto Forum compliant TR-069 based management control
- Glueless interface to most WCDMA femtocell RF solutions for cheapest BOM

Description

The picoChip PC302 is a highly integrated, high performance baseband processor for 3GPP femtocells. The PC302 together with picoChip software offers the cheapest single chip solution for up to 4 users for residential and SME femtocell access points. The programmable platform can support up to 14Mbps HSDPA together with 5.6Mbps HSUPA. The PC302 is a low power, low cost complete femtocell solution with integrated PHY, RNC stack, synchronization, network listen and Radio Resource Management solutions available. The unique multi-core architecture also allows flexibility for in field upgrade and spare processing power for customer enhancement and home networking functions.